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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sunil Talwar et al.

Title: LOGIC CIRCUIT AND METHOD FOR CARRY AND SUM GENERATION AND METHOD OF DESIGNING SUCH A LOGIC CIRCUIT

Docket No.: 1365.063US1

Serial No.: 10/714,408

Filed: November 14, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2124


MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):


- ☒ A return postcard.
- ☒ A Communication Concerning Related Applications (2 pgs.).
- ☒ A Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 5 cited documents.

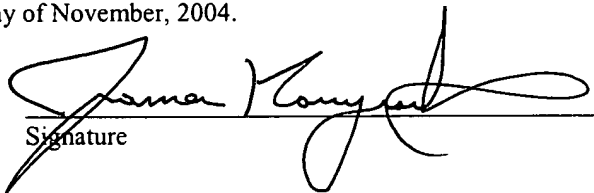
If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: 
Atty: Timothy B Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29th day of November, 2004.


Name


Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)



S/N 10/714,408

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sunil Talwar et al. Examiner: Unknown
Serial No.: 10/714,408 Group Art Unit: 2124
Filed: November 14, 2003 Docket: 1365.063US1
Title: LOGIC CIRCUIT AND METHOD FOR CARRY AND SUM GENERATION
AND METHOD OF DESIGNING SUCH A LOGIC CIRCUIT

COMMUNICATION CONCERNING RELATED APPLICATIONS

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/637532	August 11, 2000	1365.033US1	A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT
09/769954	January 25, 2001	1365.039US1	A PARALLEL COUNTER AND A MULTIPLICATION LOGIC CIRCUIT
09/898752	July 3, 2001	1365.048US1	MULTIPLICATION LOGIC CIRCUIT
09/917257	July 27, 2001	1365.051US1	A PARALLEL COUNTER AND A LOGIC CIRCUIT FOR PERFORMING MULTIPLICATION
10/027237	December 20, 2001	1365.059US1	LOGIC CIRCUITS FOR PERFORMING MODULAR MULTIPLICATION AND EXPONENTIATION
10/472658	September 22, 2003	1365.072US1	A MULTIPLICATION LOGIC CIRCUIT
10/755723	January 12, 2004	1365.067US1	A SUM BIT GENERATION CIRCUIT
10/757712	January 14, 2004	1365.064US1	A LOGIC CIRCUIT

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/714,408

Filing Date: November 14, 2003

Title: LOGIC CIRCUIT AND METHOD FOR CARRY AND SUM GENERATION AND METHOD OF DESIGNING SUCH A LOGIC CIRCUIT

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Dkt: 1365.063US1

10/774363 February 6, 1365.066US1 METHOD AND DEVICE FOR
2004 INVOLVING MULTIPLICATION OF
SELECTIVELY PARTITIONED
BINARY INPUTS USING BOOTH
ENCODING

10/776938 February 1365.065US1 LOGIC CIRCUITS FOR PERFORMING
11, 2004 THRESHOLD FUNCTIONS

Respectfully submitted,

SUNIL TALWAR ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Minneapolis, MN 55402
(612) 349-9587

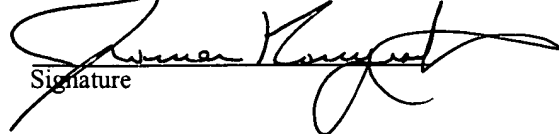
Date 22 Nov '04

By 

Timothy B. Clise
Reg. No. 40,957

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JAMES KAN YUSIK
Name


Signature

S/N 10/714,408



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Sunil Talwar et al.	Examiner:	Unknown
Serial No.:	10/714,408	Group Art Unit:	2124
Filed:	November 14, 2003	Docket:	1365.063US1
Title:	LOGIC CIRCUIT AND METHOD FOR CARRY AND SUM GENERATION AND METHOD OF DESIGNING SUCH A LOGIC CIRCUIT		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

Applicant encloses three search reports. One search report relating to GB0405653.7 is part of the present family of applications. The other two search reports relate to a family of applications that include U.S. Serial No. 10/757,712, currently pending.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No : 10/714,408

Filing Date: November 14, 2003

Title: LOGIC CIRCUIT AND METHOD FOR CARRY AND SUM GENERATION AND METHOD OF DESIGNING SUCH A LOGIC CIRCUIT

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Dkt: 1365.063US1

The present application is a U.S. national patent application filed after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

SUNIL TALWAR ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9587

Date

22 Nov. '04

By



Timothy B Clise

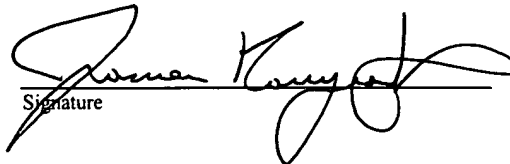
Reg. No. 40,957

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Name

JAMES KAUYUSIK

Signature

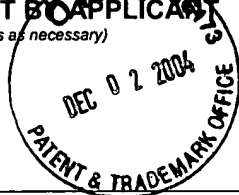


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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/714,408
Filing Date	November 14, 2003
First Named Inventor	Talwar, Sunil
Group Art Unit	2124
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 1365.063US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2003/0120694 A1	06/26/2003	Qi, Jieming	12/20/2001
	US-2004/0225705 A1	11/11/2004	Rumynin, D.	02/06/2004
	US-3,711,692	01/16/1973	Batcher, K. E.	03/15/1971
	US-6,430,251	08/06/2002	Gold, S. M.	10/24/2000
	US-6,470,443	10/22/2002	Emer, J. S., et al.	03/14/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
	EP-0992882A2	04/12/2000	Laurenti, G., et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"PCT International Search Report relating to PCT/GB2004/000135", (June 17, 2004), 4 Pages	
		"United Kingdom Search Report relating to Great Britain Patent Application No. GB0405653.7", (July 7, 2004), 3 Pages	
		"United Kingdom Search Report relating to Great Britain Patent Application No. GB0400799.3", (June 23, 2004), 1 Page	
		TIETZE, U., et al., "Halbleiterschaltungstechnik", (1993), 226-227, 273-276	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached